

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Douglas Brisbin et al.

Application No.: 10/616,381

Filed: July 9, 2003

LDMOS TRANSISTOR STRUCTURE

FOR IMPROVING HOT CARRIER

RELIABILITY

Confirmation No.: 1278

Group No.: 2811

Examiner: Unknown

SUBMISSION OF POWER OF ATTORNEY BY ASSIGNEE

PATENT

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed for filing please find a duly executed Power of Attorney by Assignee for the Sir: above-identified application which specifies an Assignment recordal date of November 10, 2003, at Reel 14677, Frame 71, and shows Assignee's entire right, title and interest to establish ownership.

Respectfully submitted,

STALLMAN & POLLOCK LLP

July 28, 2004 Date:

By: Brian J. Keating (Reg. No. 39,520)

Attorneys for Applicant(s)

Atty Docket No.: NSC1-M2900 [P05632]



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NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the Sir: above-identified application by assignment dated 10/28/2003, 11/03/2003, 10/31/2003 and 10/28/2003, which assignment was recorded in the Patent and Trademark Office on November 10, 2003, at Reel 14677, Frame 71, hereby appoints the members of the firm of STALLMAN & POLLOCK LLP, a firm composed of:

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as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

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Atty Docket No.: NSC1-M2900 [P05632]